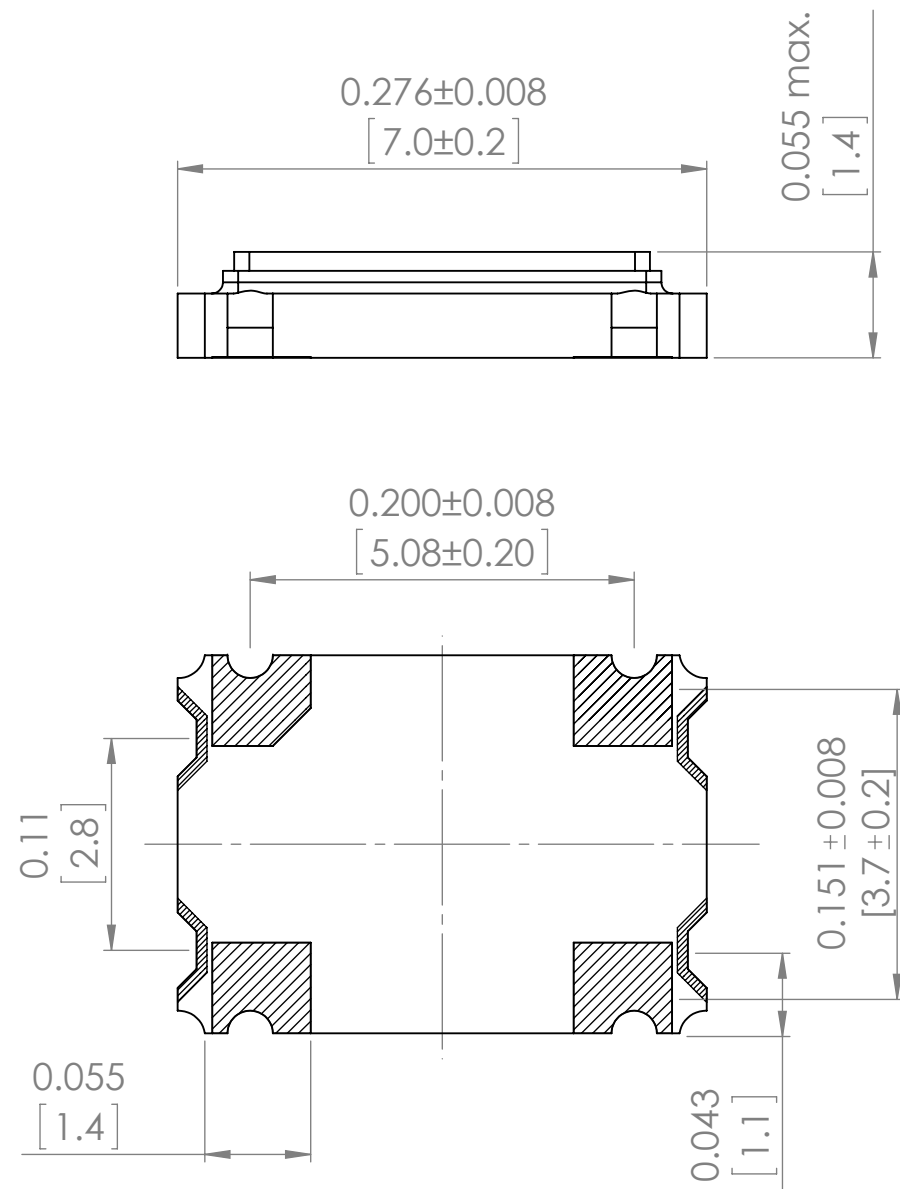
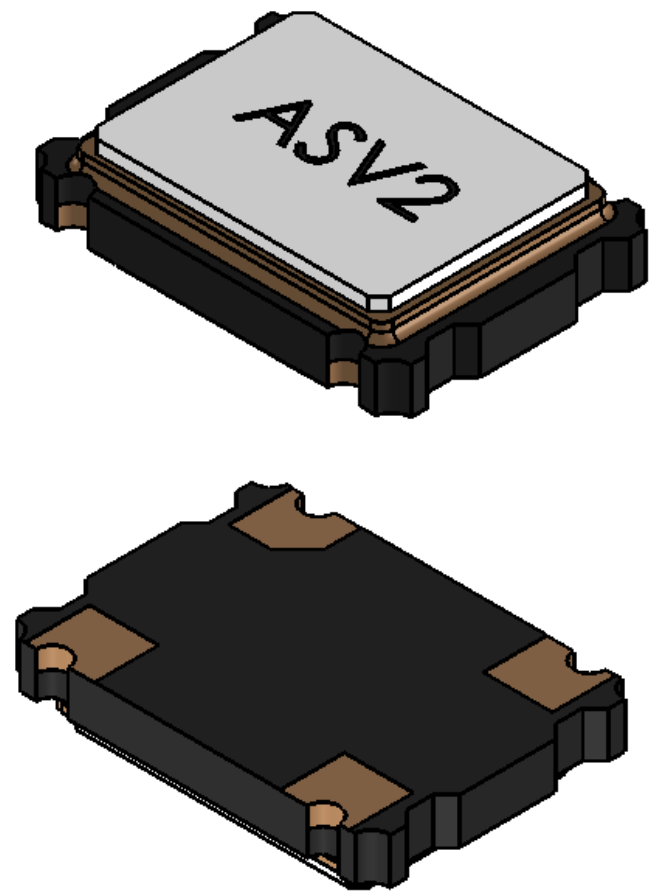
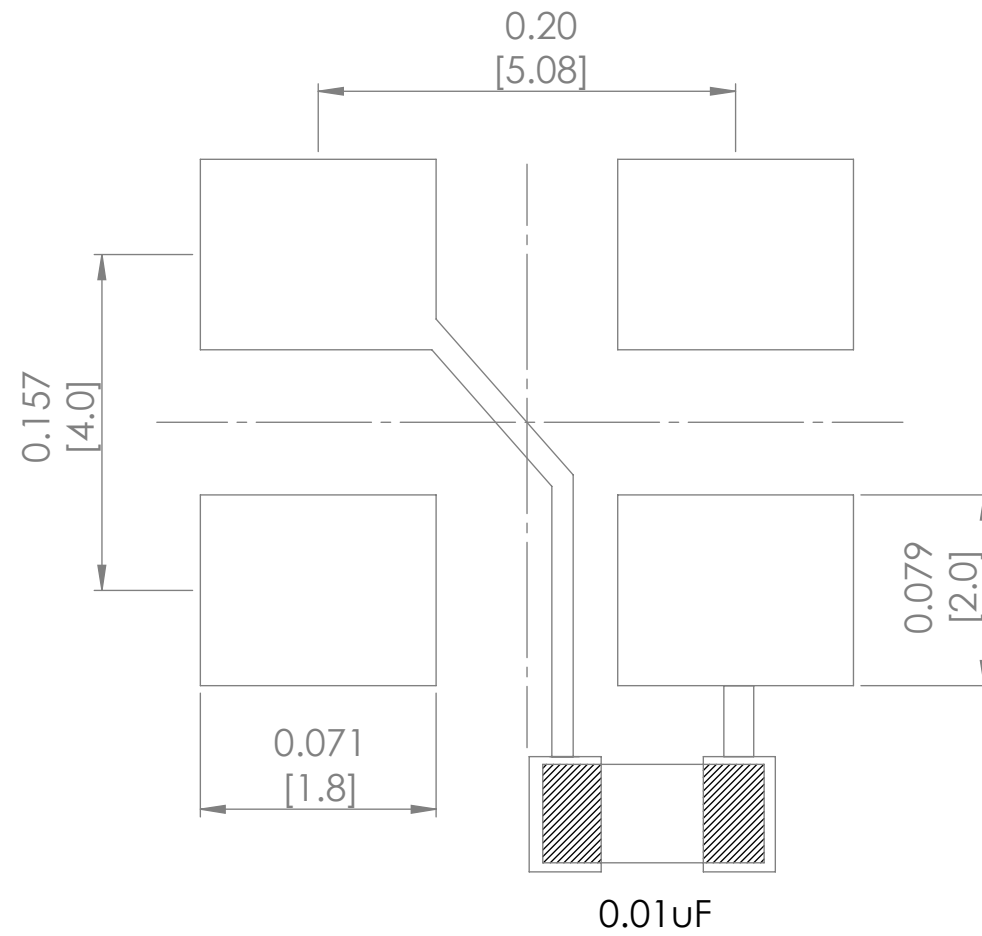



Pin	Function
1	Tristate Enable/Disable
2	GND/Case
3	Output
4	Vdd



**Recommended Land Pattern**



Note: It is recommended to use an approximately 0.01µF bypass capacitor between PIN 2 and 4.

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN MM SURFACE FINISH: TOLERANCES: LINEAR: ANGULAR:				FINISH:		DEBUR AND BREAK SHARP EDGES		DO NOT SCALE DRAWING		REVISION	
DRAWN SAAZVAT				SIGNATURE		DATE		 30332 Esperanza, Rancho Santa margarita, California 92688			
CHK'D XXXXXX								TITLE: Oscillator			
APPV'D								DWG NO. ASV2			
MFG								SCALE:4:1			
Q.A						MATERIAL:		SHEET 1 OF 1			
						WEIGHT:		A3			